LG Display

LC230EUE
Liquid Crystal Display

Product Specification

SPECIFICATION
FOR
APPROVAL

( ) **Preliminary Specification**
(●) **Final Specification**

Title	23" Full HD TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LC230EUE
SUFFIX	SEA1

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY		SIGNATURE DATE
	/	
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Please return 1 copy for your confirmation with your signature and comments.

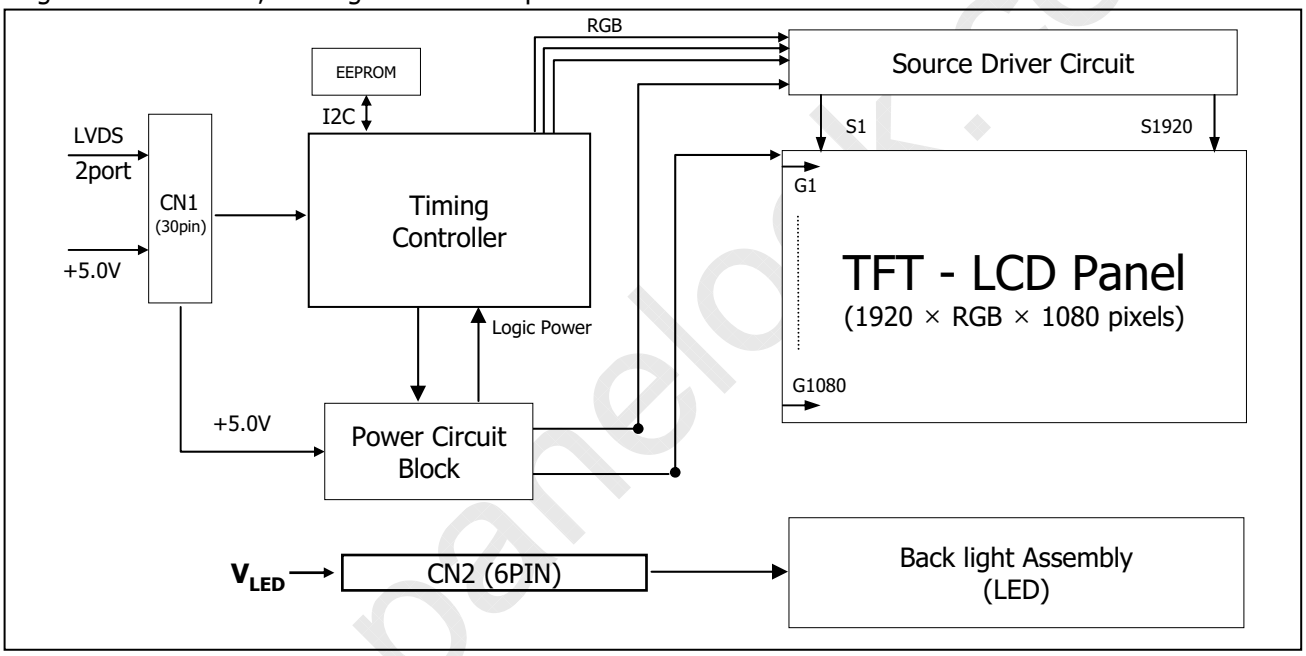
Contents

RECORD OF REVISIONS

Revision No	Revision Date	Page	Description
0.0	Nov, 09, 2011	-	First Draft, **Preliminary Specifications**
0.1	Dec, 05, 2011	19	Update Table 9. OPTICAL CHARACTERISTICS
1.0	Jan, 10, 2012		Final CAS release

1. General Description

LC230EUE is a Color Active Matrix Liquid Crystal Display with a Light Emitting Diode (White LED) backlight system without LED driver. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 23 inch diagonally measured active display area with FHD resolution (1080 vertical by 1920horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,7M colors with A-FRC (Advanced Frame Rate Control).
It has been designed to apply the 8Bit 2 port LVDS interface.
It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



[Figure 1] Block diagram

General Features

Active Screen Size	23 inches(58.42cm) diagonal
Outline Dimension	533.2(H) x 312.0(V) x 10.5(D) mm (Typ.)
Pixel Pitch	0.2652 mm x 0.2652 mm
Pixel Format	1920 horiz. By 1080 vert. Pixels RGB stripes arrangement
Color Depth	16,7M colors (6bit + A-FRC)
Luminance, White	250 cd/m^2 (Center 1 Point, Typ.)
Viewing Angle(CR>10)	View Angle Free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 17.4 Watt (Typ.) (4.5 Watt @V_{LCD}, 12.9 Watt @Is=120mA)
Weight	2,200g (typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)

2. Absolute Maximum Ratings

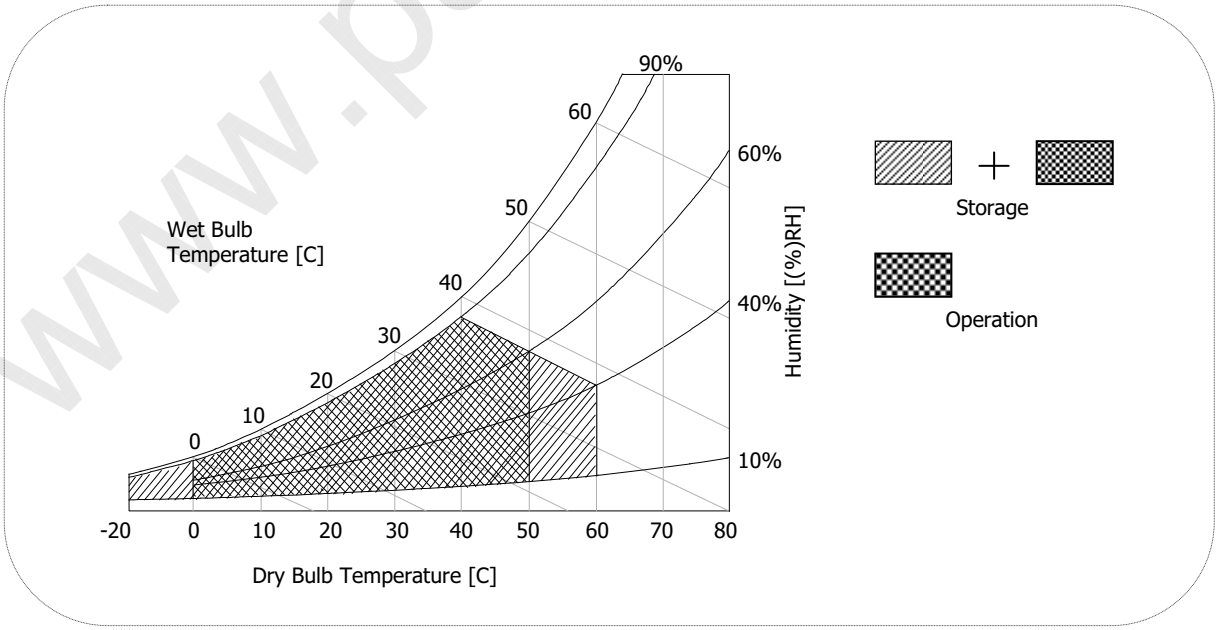
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Input Voltage	VLCD	-0.3	6.0	Vdc	at 25 ± 2°C
Operating Temperature	TOP	0	50	°C	1, 2, 3, 4
Storage Temperature	TST	-20	60	°C	
Operating Ambient Humidity	HOP	10	90	%RH	
Storage Humidity	HST	10	90	%RH	
Panel Front Temperature	TSUR	-	+68	°C	5

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 °C Max, and no condensation of water.
2. Maximum Storage Humidity is up to 40℃, 70% RH only for 4 corner light leakage Mura.
3. Storage condition is guaranteed under packing condition
4. Gravity mura can be guaranteed below 40°C condition
5. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68℃. The range of operating temperature may be degraded in case of improper thermal management in final product design.

FIG.2 Temperature and relative humidity



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

Table 2-1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
MODULE :						
Power Supply Input Voltage	V_{LCD}	4.5	5	5.5	Vdc	
Permissive Power Input Ripple	V_{dRF}			100	mV_{p-p}	1
Power Supply Input Current	I_{LCD}	-	(890)	(1110)	mA	2
		-	(1060)	(1320)	mA	3
Power Consumption	$P_{C\ TYP}$	-	(4.5)	(5.6)	Watt	2
	$P_{C\ MAX}$	-	(5.3)	(6.6)	Watt	3
Rush current	I_{RUSH}	-	-	3.0	A	4

Note :

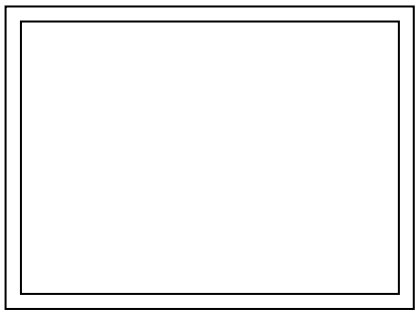
1. Permissive power ripple should be measured under V_{LCD} =5.0V, 25°C, fV(frame frequency)=MAX condition and At that time, we recommend the bandwidth configuration of oscilloscope is to be under 20Mhz. See the next page.

2. The specified current and power consumption are under the V_{LCD}=5.0V, 25± 2°C,fV=60Hz condition whereas Typical Power Pattern [Mosaic] shown in the [Figure 3] is displayed.

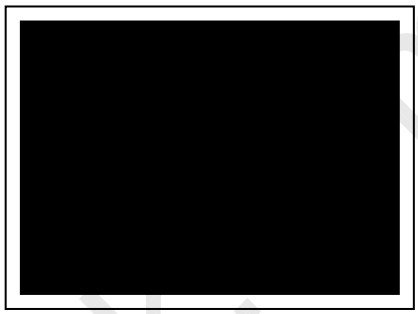
3. The current is specified at the maximum current pattern.

4. Maximum Condition of Inrush current :
   The duration of rush current is about 5ms and rising time of power Input is 500us ± 20%.(min.).

• **Permissive Power input ripple (**V_{LCD} =5.0V, 25°C, fv (frame frequency)=MAX condition**)**

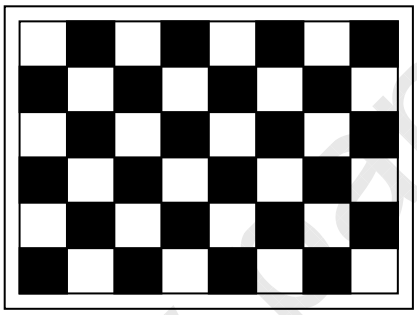


White pattern

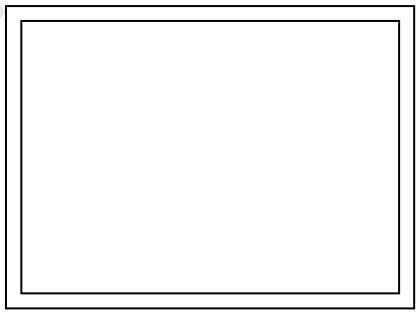


Black pattern

• **Power consumption (**V_{LCD} =5V, 25°C, fV (frame frequency=60Hz condition**)**



Typical power Pattern



Maximum power Pattern

FIG.3 Mosaic pattern & White Pattern for power consumption measurement

Table 2-2. LED Bar ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
		Min.	Typ.	Max.		
LED String Current	Is	-	120	130	mA	1, 2, 5
LED String Voltage	Vs	50.2	53.6	57.0	V	1, 5
Power Consumption	P_{Bar}	-	12.9	13.7	Watt	1, 2, 4
LED Life Time	LED_LT	30,000	-	-	Hrs	3

Notes) The LED Bar consists of 34 LED packages, 2 strings (parallel) x 17 packages (serial)

LED driver design guide

: The design of the LED driver must have specifications for the LED in LCD Assembly.
The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED driver.
So all the parameters of an LED driver should be carefully designed and output current should be Constant current control.
Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.
When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs.
When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

1. The specified values are for a single LED bar.
2. The specified current is defined as the input current for a single LED string with 100% duty cycle.
3. The LED life time is defined as the time when brightness of LED packages become 50% or less than the initial value under the conditions at Ta = 25 ± 2°C and LED string current is typical value.
4. The power consumption shown above does not include loss of external driver.
   The typical power consumption is calculated as P_{Bar} = Vs(Typ.) x Is(Typ.) x No. of strings.
   The maximum power consumption is calculated as P_{Bar} = Vs(Max.) x Is(Typ.) x No. of strings.
5. LED operating conditions are must not exceed Max. ratings.

3-2. Interface Connections

3-2-1. LCD Module

- LCD Connector(CN1) : IS100-L30O-C23 (UJU) , GT103-30S-HF15 (LSM)
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Symbol
1	FR0M	Minus signal of odd channel 0 (LVDS)	16	SR1P	Plus signal of even channel 1 (LVDS)
2	FR0P	Plus signal of odd channel 0 (LVDS)	17	GND	Ground
3	FR1M	Minus signal of odd channel 1 (LVDS)	18	SR2M	Minus signal of even channel 2 (LVDS)
4	FR1P	Plus signal of odd channel 1 (LVDS)	19	SR2P	Plus signal of even channel 2 (LVDS)
5	FR2M	Minus signal of odd channel 2 (LVDS)	20	SCLKINM	Minus signal of even clock channel (LVDS)
6	FR2P	Plus signal of odd channel 2 (LVDS)	21	SCLKINP	Plus signal of even clock channel (LVDS)
7	GND	Ground	22	SR3M	Minus signal of even channel 3 (LVDS)
8	FCLKINM	Minus signal of odd clock channel (LVDS)	23	SR3P	Plus signal of even channel 3 (LVDS)
9	FCLKINP	Plus signal of odd clock channel (LVDS)	24	GND	Ground
10	FR3M	Minus signal of odd channel 3 (LVDS)	25	NC	No Connection (I2C Serial interface for LCM)
11	FR3P	Plus signal of odd channel 3 (LVDS)	26	NC	No Connection.(I2C Serial interface for LCM)
12	SR0M	Minus signal of even channel 0 (LVDS)	27	PWM_OUT	For Control Burst frequency of Inverter
13	SR0P	Plus signal of even channel 0 (LVDS)	28	V_{LCD}	Power Supply +5.0V
14	GND	Ground	29	V_{LCD}	Power Supply +5.0V
15	SR1M	Minus signal of even channel 1 (LVDS)	30	V_{LCD}	Power Supply +5.0V

Note: 1. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
2. All V_{LCD} (power input) pins should be connected together.
3. Input Level of LVDS signal is based on the IEA 664 Standard.
4. PWM_OUT signal controls the burst frequency of a inverter.
This signal is synchronized with vertical frequency.
It's frequency is 3 times of vertical frequency, and it's duty ratio is 50%.
If you don't use this pin, it is no connection.

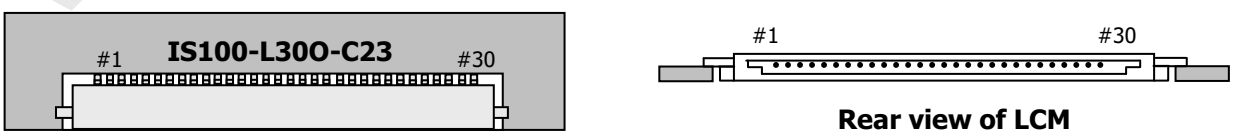


FIG.4 Connector diagram

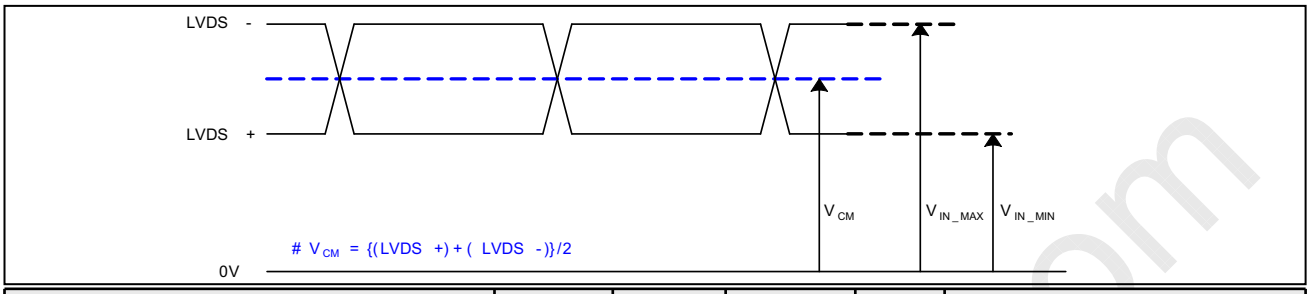
Table 4. REQUIRED SIGNAL ASSIGNMENT FOR Flat Link (TI:SN75LVDS83) Transmitter

Pin #	Pin Name	Require Signal	Pin #	Pin Name	Require Signal
1	Vcc	Power Supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input (R7)	30	D26	TTL Input (DE)
3	D6	TTL Input (R5)	31	T_X CLKIN	TTL Level clock Input
4	D7	TTL Input (G0)	32	PWR DWN	Power Down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input (G1)	34	PLL Vcc	Power Supply for PLL
7	D9	TTL Input (G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input (G6)	36	LVDS GND	Ground pin for LVDS
9	Vcc	Power Supply for TTL Input	37	TxOUT3+	Positive LVDS differential data output 3
10	D11	TTL Input (G7)	38	TxOUT3−	Negative LVDS differential data output 3
11	D12	TTL Input (G3)	39	T_X CLKOUT+	Positive LVDS differential clock output
12	D13	TTL Input (G4)	40	T_X CLKOUT−	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	T_X OUT2+	Positive LVDS differential data output 2
14	D14	TTL Input (G5)	42	T_X OUT2−	Negative LVDS differential data output 2
15	D15	TTL Input (B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input (B6)	44	LVDS Vcc	Power Supply for LVDS
17	Vcc	Power Supply for TTL Input	45	T_X OUT1+	Positive LVDS differential data output 1
18	D17	TTL Input (B7)	46	T_X OUT1−	Negative LVDS differential data output 1
19	D18	TTL Input (B1)	47	T_X OUT0+	Positive LVDS differential data output 0
20	D19	TTL Input (B2)	48	T_X OUT0−	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input (B3)	50	D27	TTL Input (R6)
23	D21	TTL Input (B4)	51	D0	TTL Input (R0)
24	D22	TTL Input (B5)	52	D1	TTL Input (R1)
25	D23	TTL Input (RSVD)	53	GND	Ground pin for TTL
26	Vcc	Power Supply for TTL Input	54	D2	TTL Input (R2)
27	D24	TTL Input (HSYNC)	55	D3	TTL Input (R3)
28	D25	TTL Input (VSYNC)	56	D4	TTL Input (R4)

Notes : 1. Refer to LVDS Transmitter Data Sheet for detail descriptions.
2. 7 means MSB and 0 means LSB at R,G,B pixel data

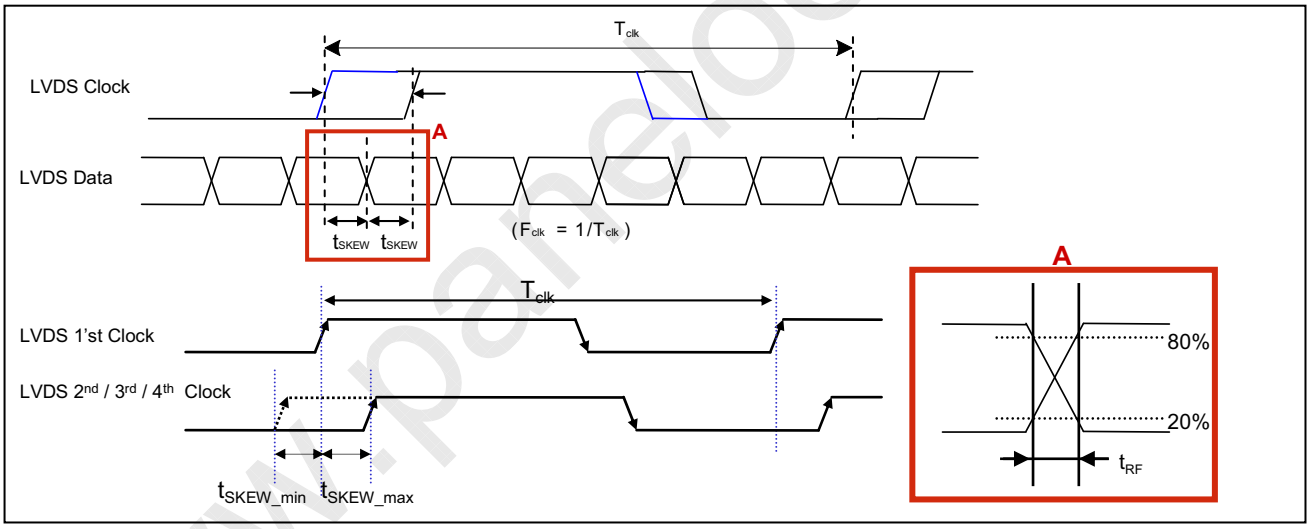
LVDS Input characteristics

1. DC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Common mode Voltage	V_{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V_{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔVCM	-	250	mV	-

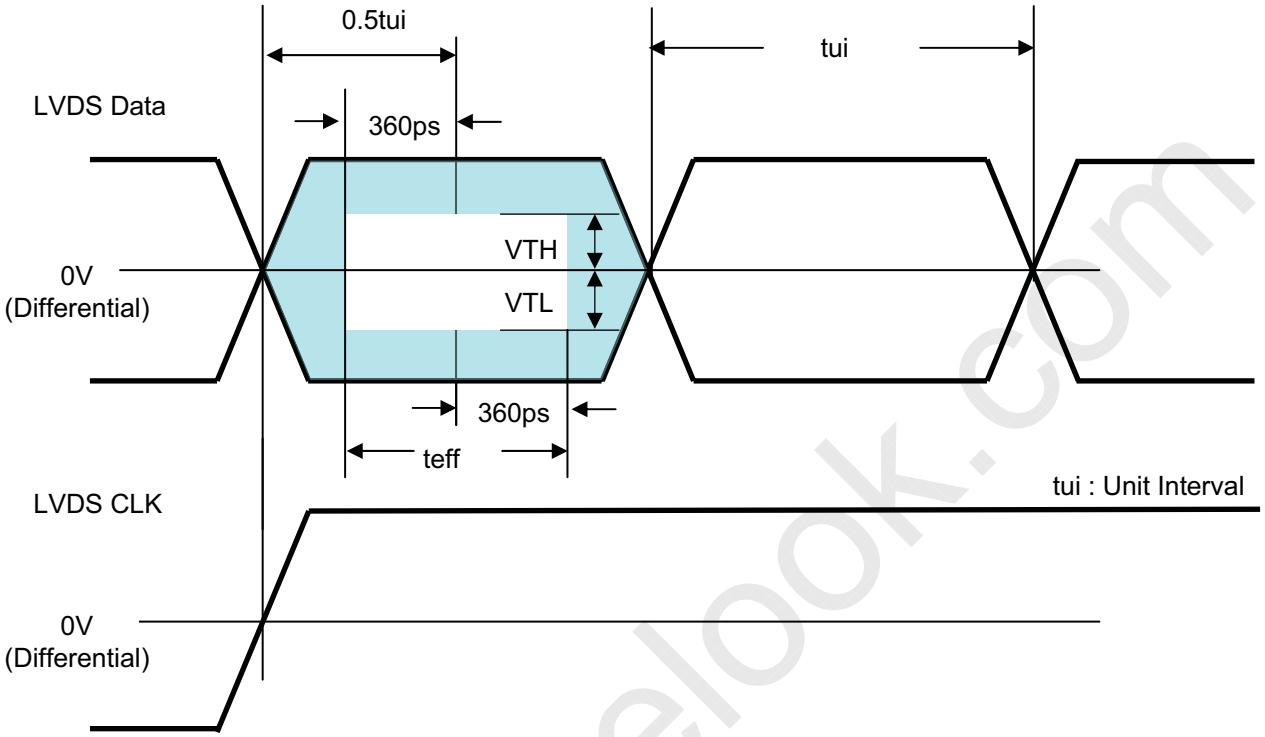
2. AC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Differential Voltage	V_{TH}	100	600	mV	Tested with Differential Probe 3
	V_{TL}	-600	-100	mV	
LVDS Clock to Data Skew	t_{SKEW}	-	$\|(0.25*T_{clk})/7\|$	ps	-
LVDS Clock/DATA Rising/Falling time	t_{RF}	260	$\|(0.3*T_{clk})/7\|$	ps	2
Effective time of LVDS	t_{eff}	$\|\pm 360\|$	-	ps	-
LVDS Clock to Clock Skew (Even to Odd)	t_{SKEW_EO}	-	$\|1/7* T_{clk}\|$	ps	-

Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.
2. If t_{RF} isn't enough, t_{eff} should be meet the range.
3. LVDS Differential Voltage is defined within t_{eff}

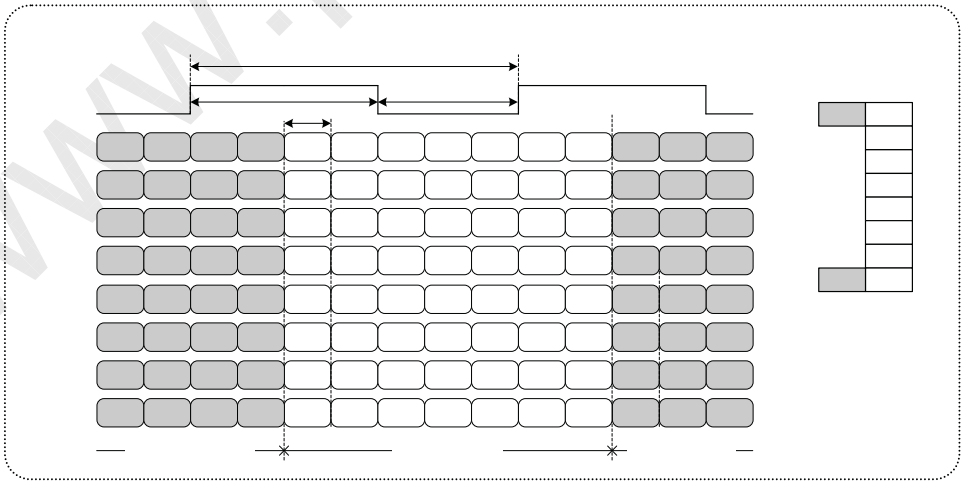
Note



* This accumulated waveform is tested with differential probe

3. Data Format

1) LVDS 2 Port

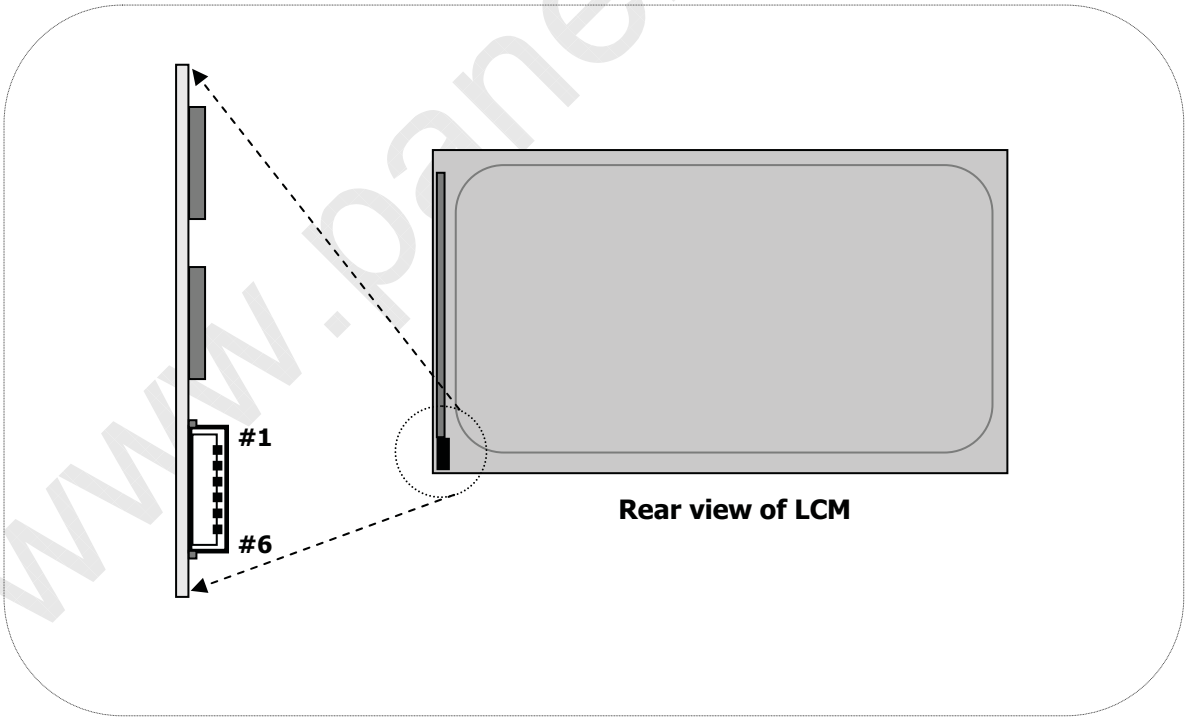


< LVDS Data Format >

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2)

The LED interface connector is a model SM06B-SHJH(HF) manufactured by JST.
The mating connector is a SHJP-06V-S(HF) or Equivalent.
The pin configuration for the connector is shown in the table below.

Pin	Symbol	Description	Notes
1	FB1	Channel1 Current Feedback	
2	NC	No Connection	
3	VLED	LED Power Supply	
4	VLED	LED Power Supply	
5	NC	No Connection	
6	FB2	Channel2 Current Feedback	



[Figure 5] Backlight connector view

3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	tHV	960	960	960	tCLK	1920 / 2
	Blank	tHB	100	140	240	tCLK	1
	Total	tHP	1060	1100	1200	tCLK	
Vertical	Display Period	tVV	1080	1080	1080	Lines	
	Blank	tVB	20 (228)	45 (270)	69 (300)	Lines	1
	Total	tVP	1100 (1308)	1125 (1350)	1149 (1380)	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	fCLK	63.00	74.25	78.00	MHz	
	Horizontal	fH	57.3	67.5	70	KHz	2
	Vertical	fV	57 (47)	60 (50)	63 (53)	Hz	2 NTSC : 57~63Hz (PAL : 47~53Hz)

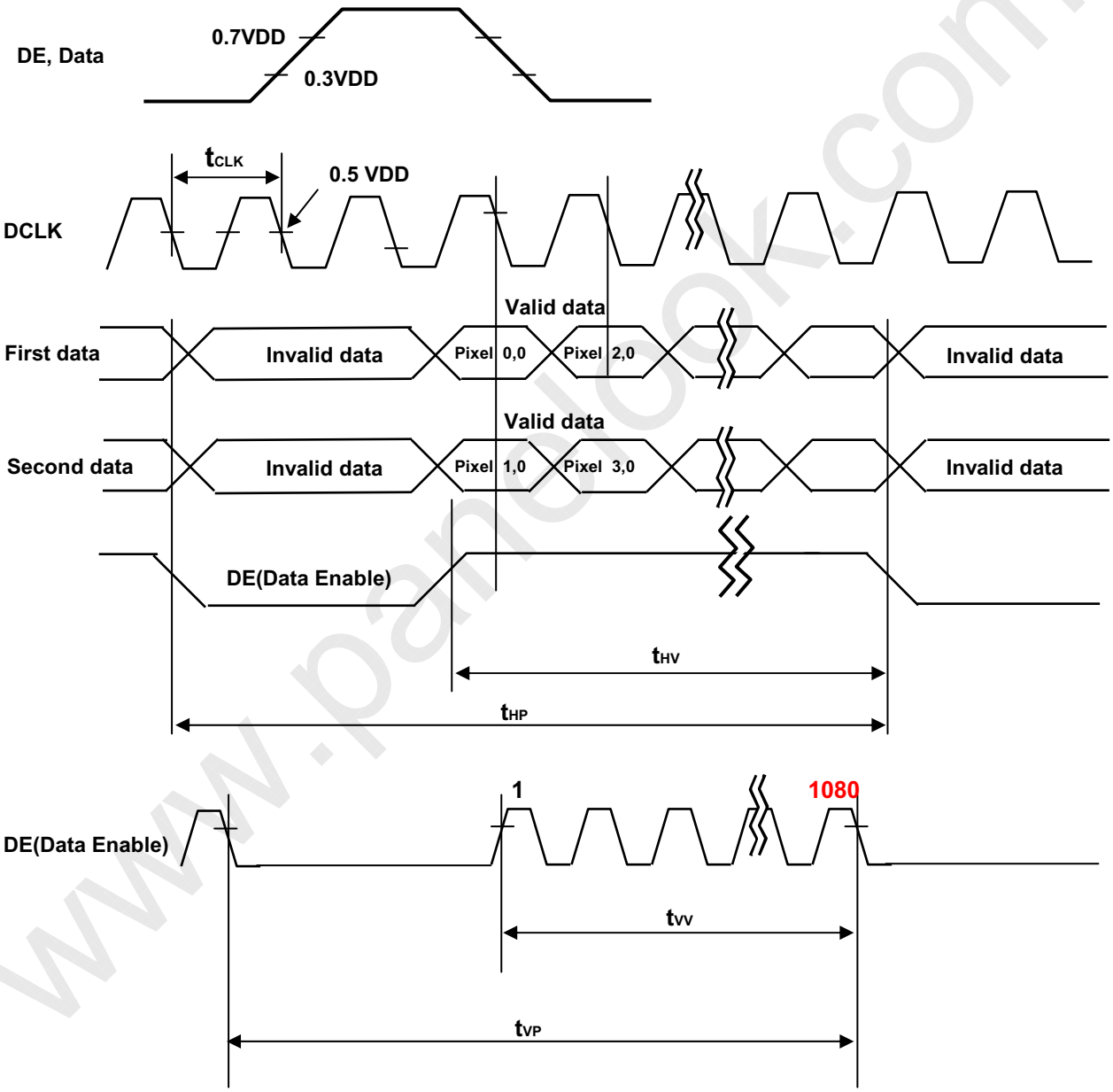
Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency

※ Timing should be set based on clock frequency.

3-4. Signal Timing Waveforms

1. DCLK, DE, DATA waveforms



3-5. Color Input Data Reference

The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB							LSB	MSB							LSB	MSB							LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...					...								...								...			
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	...					...								...								...			
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	...					...								...								...			
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

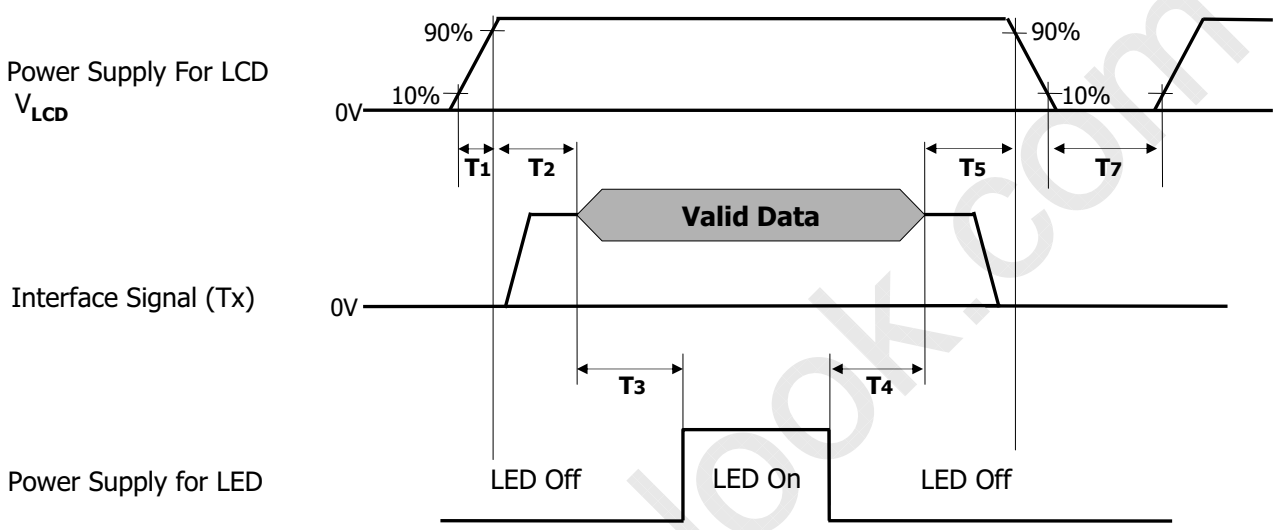


Table 8. POWER SEQUENCE

Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0.01	-	50	ms
T3	500	-	-	ms
T4	200	-	-	ms
T5	0.01	-	50	ms
T7	1000		-	ms

Notes : 1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.
3. The invalid signal means out of the signal timing specification which define as page 14.
4. The above power sequence should be satisfied the basic power on/off and resolution, timing transition.
5. LED power must be turn on after power supply for LCD and interface signal are valid.

3-7. V_{LCD} Power Dip Condition

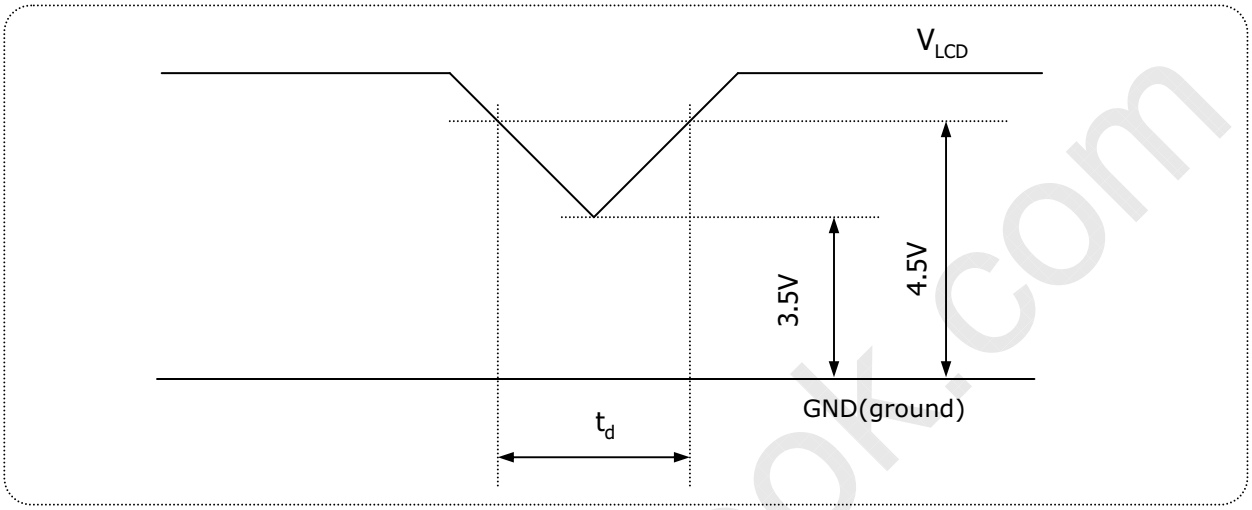


FIG.6 Power dip condition

1) Dip condition

$3.5V \leq V_{LCD} < 4.5V$, $t_d \leq 20ms$

2) $V_{LCD} < 3.5V$

V_{LCD}-dip conditions should also follow the Power On/Off conditions for supply voltage.

4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at 25±2°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 ° and aperture 1 degree.
FIG. 1 presents additional information concerning the measurement equipment and method.

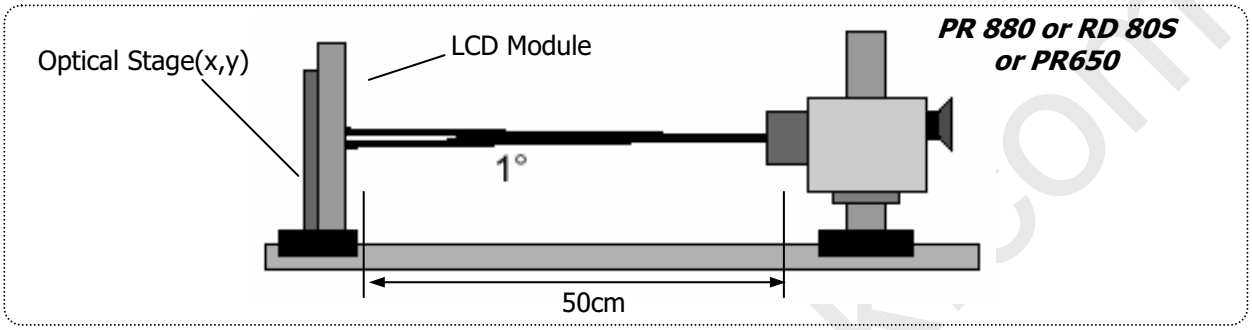


FIG.7 Optical Characteristic Measurement Equipment and Method

Table 9. OPTICAL CHARACTERISTICS (Ta=25 °C, V_{LCD}=5V, f_V=60Hz Dclk=144MHz, I_{BL}=120mA)

Parameter		Symbol	Values			Units	Notes
			Min	Typ	Max		
Contrast Ratio		CR	600	1000	-		1
Surface Luminance, white		L_{WH}	200	250	-	cd/m^2	2
Luminance Variation		δ_{WHITE}	-	-	1.3		3
Response Time	Gray To Gray	T_{GTG_AVR}	-	14	25	ms	4
	Gray to Gray (σ)	G to G σ	-	(5)	-	ms	Reference 10,11
Color Coordinates [CIE1931] ***(By PR650)***	RED	Rx	Typ -0.03	0.636	Typ +0.03		
		Ry		0.334			
	GREEN	Gx		0.309			
		Gy		0.623			
	BLUE	Bx		0.150			
		By		0.067			
	WHITE	Wx		0.313			
		Wy		0.329			
Viewing Angle (CR>10)							
General	Horizontal	θ_H	170	178	-	Degree	5
	Vertical	θ_V	170	178	-		
Gray Scale		-		2.2			8

Notes 1. Contrast Ratio(CR) is defined mathematically as : ***(By PR880)***

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center point(Location P1)

2. Surface luminance(L_{WH})is luminance value at Center 1 point(P1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.8 (By PR880)

3. The variation in surface luminance , δ WHITE is defined as : ***(By PR880)***
δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5})
Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .
For more information, see the FIG. 8.

4. Gray to gray response time is the time required for the display to transition from gray to gray. For additional information see Table 10. ***(By RD80S)***

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.10 ***(By PR880)***

Notes 8. Gamma Value is approximately 2.2. For more information see Table 11.

Notes 9. It is the standard deviation of G to G (σ) data.

$$\text{G to G } (\sigma) = \frac{\sqrt{\Sigma(Xi - u)^2}}{N}$$

Xi = Individual Data
u = Data average
N : The number of Data

Notes 10. This is not used for product spec, but for end-user marketing purpose

Measuring point for surface luminance & measuring point for luminance variation.

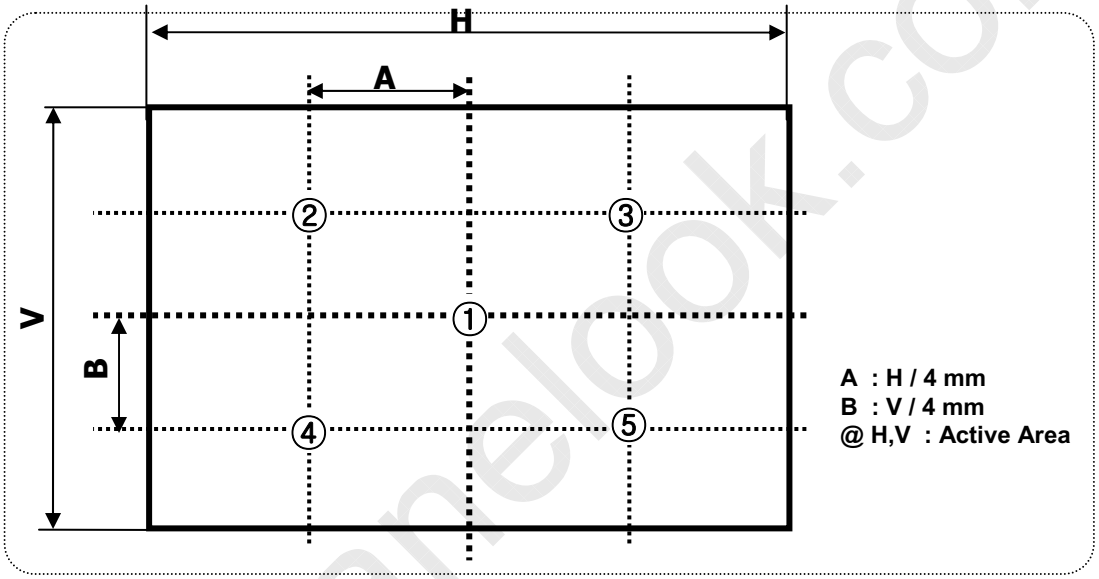


FIG.8 Measure Point for Luminance

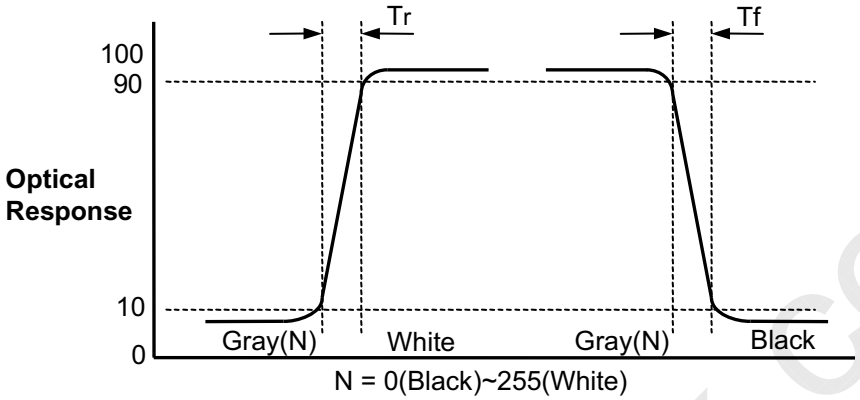
The Gray to Gray response time is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray ".

- Gray step : 5 Step
- TGTG_AVR is the total average time at rising time and falling time for "Gray To Gray ".
- if system use ODC (Over Driving Circuit) function, Gray to Gary response time may be 5ms~8ms GtG
  * it depends on Overshoot rate.

Table. 10 GTG Gray Table

Gray to Gray		Rising Time				
		G255	G191	G127	G63	G0
Falling Time	G255					
	G191					
	G127					
	G63					
	G0					

G to G(BW) Response time is defined as the following figure and shall be measured by switching the input signal for “Gray(N)” and “Black or White”.



Dimension of viewing angle range.

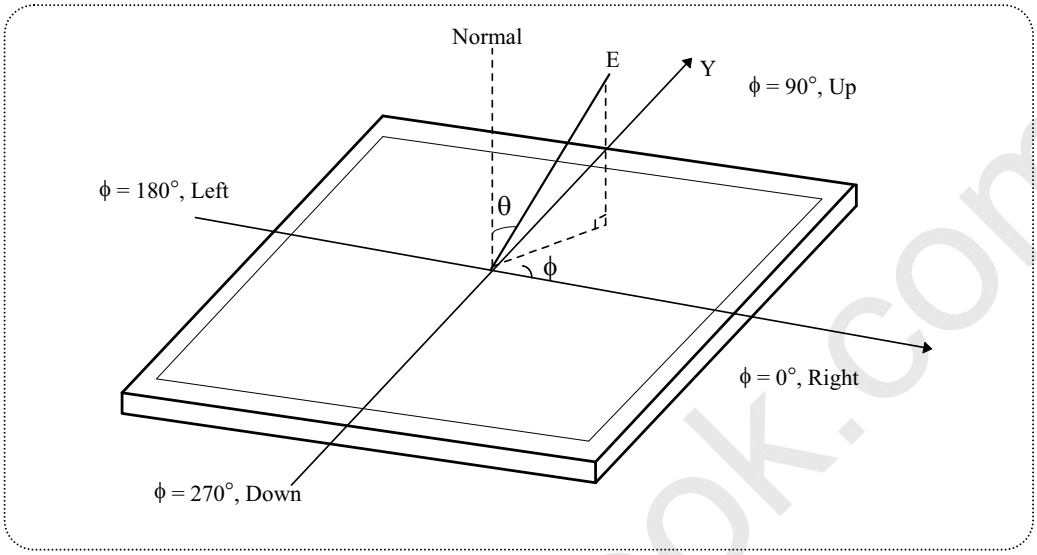


FIG.10 Viewing angle

Table 11. Gray Scale Specification

Gray Level	Relative Luminance [%] (Typ.)
0	0.11
31	1.08
63	4.72
95	11.49
127	21.66
159	35.45
191	53.00
223	74.48
255	100

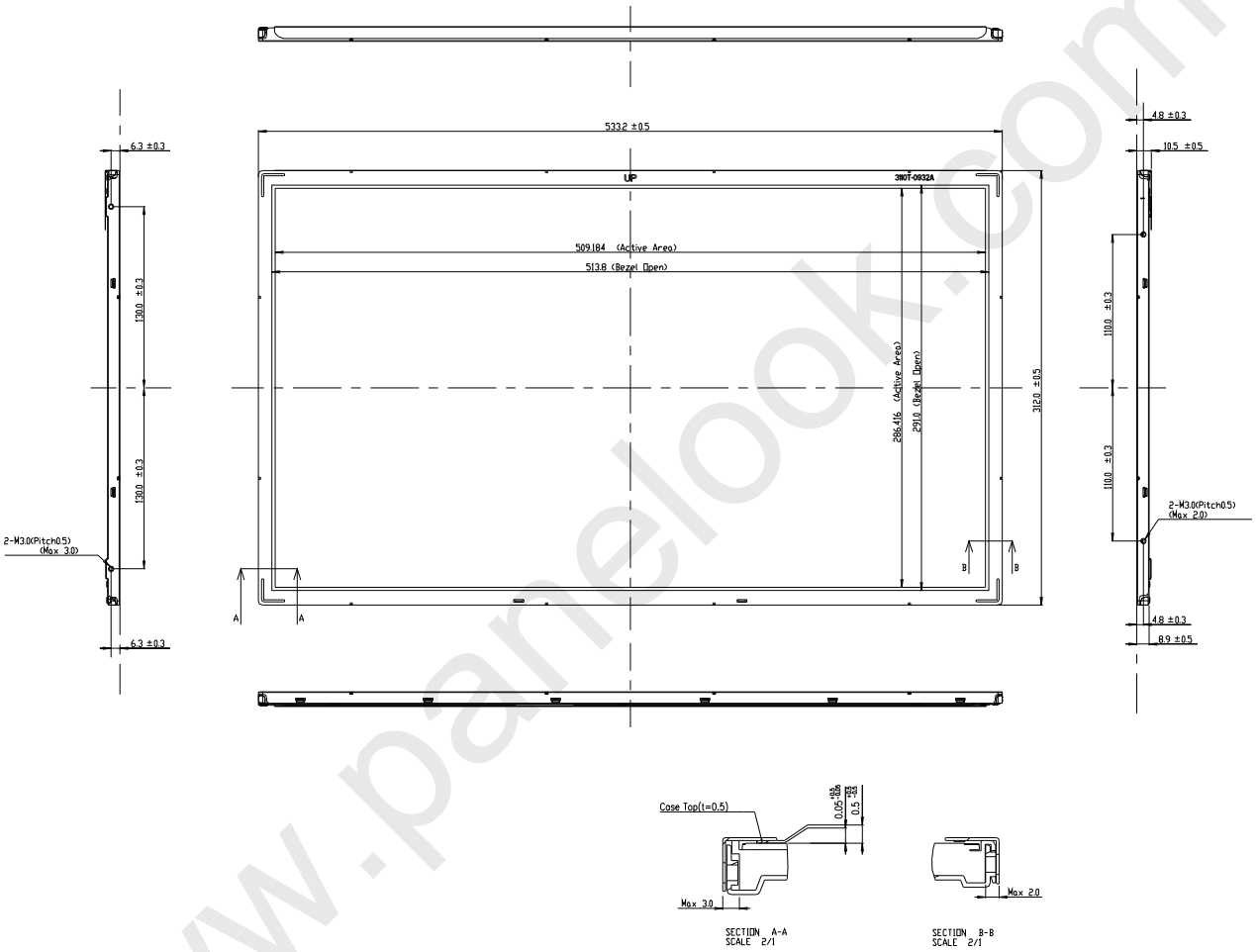
5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

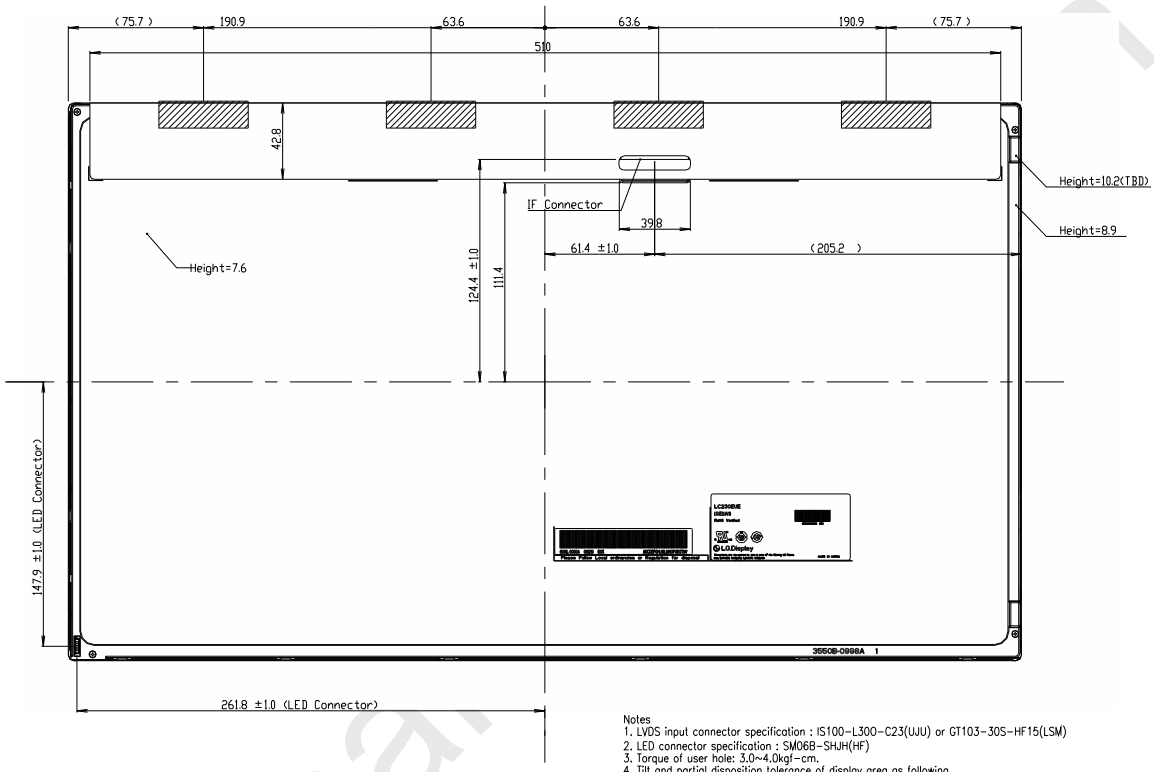
Outline Dimension	Horizontal	**533.2mm**
	Vertical	**312.0mm**
	Depth	**10.5 mm**
Bezel Area	Horizontal	**513.8mm**
	Vertical	**291.0mm**
Active Display Area	Horizontal	**509.184mm**
	Vertical	**286.416mm**
Weight	Typ : 2,200g , Max : 2,310g	
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer	

Notes : Please refer to a mechanic drawing in terms of tolerance at the next page.

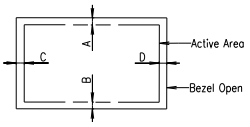
<FRONT VIEW>



<REAR VIEW>



Notes
1. LVDS input connector specification : IS100-L300-C23(UJU) or GT103-30S-HF15(LSM)
2. LED connector specification : SM06B-SHJH(HF)
3. Torque of user hole: 3.0~4.0kgf-cm.
4. Tilt and partial disposition tolerance of display area as following
   (1) Y-direction : IA-BI ≦ 1.4
   (2) X-direction : IC-DI ≦ 1.4



5. Unspecified tolerances to be ±0.5mm
6. The COF area is weak & sensive, so please don't press the COF area

LGD Highly recommendation :
As The IPS panel is sensitive & slim, please recommend the metal frame of the system supports the panel by the double side-mount.

6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.00G RMS Bandwidth : 10-300Hz Duration : X, Y, Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : ±X, ±Y, ±Z One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	 0 - 10,000 feet(3,048m) 0 - 40,000 feet(12,192m)
9	Maximum Storage Humidity for 4 corner light leakage Mura.	Max 70%RH , Ta=40℃

7. International Standards

7-1. Safety

a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
Information Technology Equipment - Safety - Part 1 : General Requirements.

b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association.
Information Technology Equipment - Safety - Part 1 : General Requirements.

c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization(CENELEC).
Information Technology Equipment - Safety - Part 1 : General Requirements.

d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC).
Information Technology Equipment - Safety - Part 1 : General Requirements.
(Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1 : 2001 Embedded LED Power (Class 1M)

2. Caution
: LED inside.
Class 1M laser (LEDs) radiation when open.
Do not open while operating.

7-2. EMC

a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.

b) CISPR 22 "Information technology equipment – Radio disturbance characteristics – Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.

c) CISPR 13 "Sound and television broadcast receivers and associated equipment – Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M

A,B,C : SIZE(INCH)
D : YEAR
E : MONTH
F ~ M : SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	A	B	C	D	E	F	G	H	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 11pcs

b) Box Size : 365 X 418 X 618

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.
(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.
(10) As The IPS panel is sensitive & slim, please recommend the metal frame of the system supports the panel by the double side-mount.

9-2. OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
V=±200mV(Over and under shoot voltage)
(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
(7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
(8) A screw which is fastened up the steels should be a machine screw.
(if not, it causes metallic foreign material and deal LCM a fatal blow)
(9) Please do not set LCD on its edge.
(10) When LCMs are used for public display defects such as Yogore, image sticking can not be guarantee.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

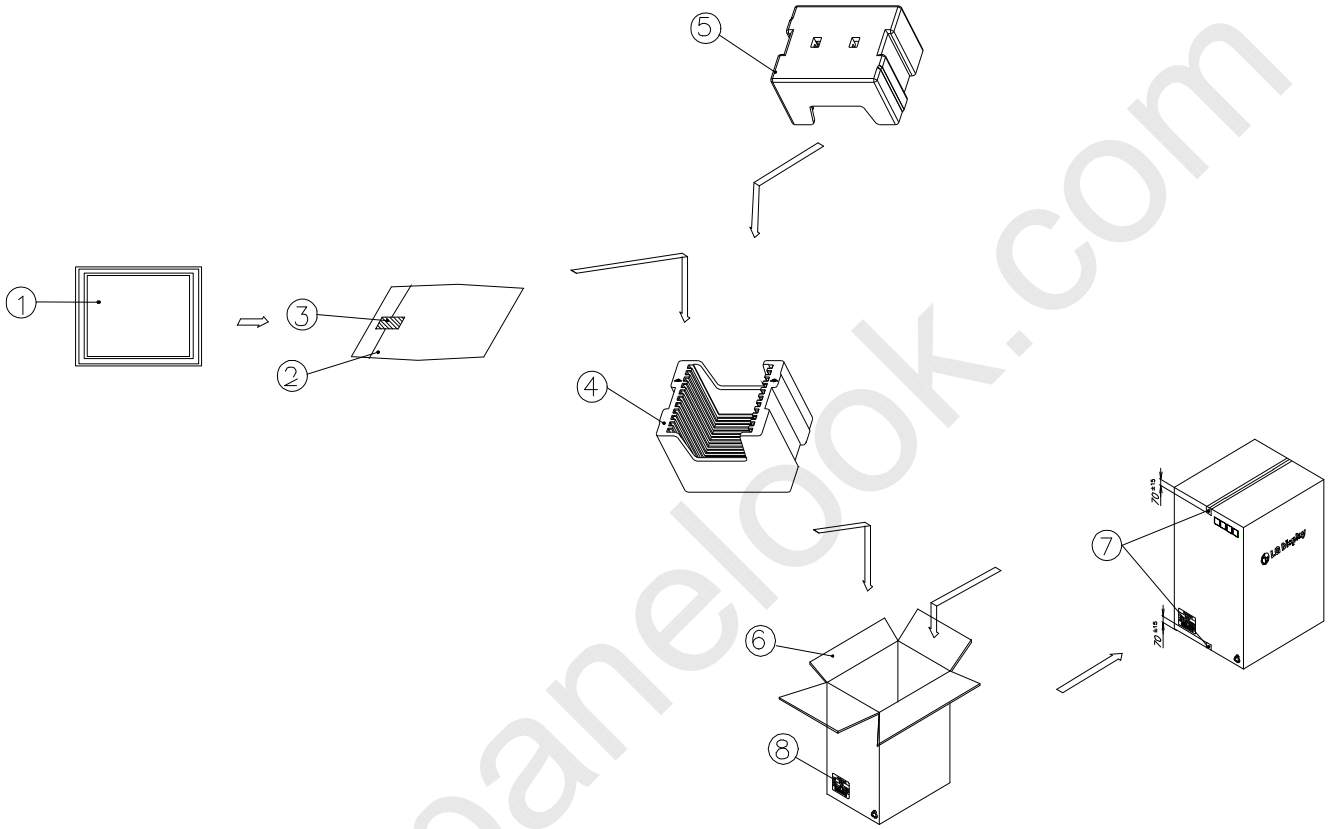
(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.
(3) Storage condition is guaranteed under packing conditions.
(4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
(3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

APPENDIX-I-1

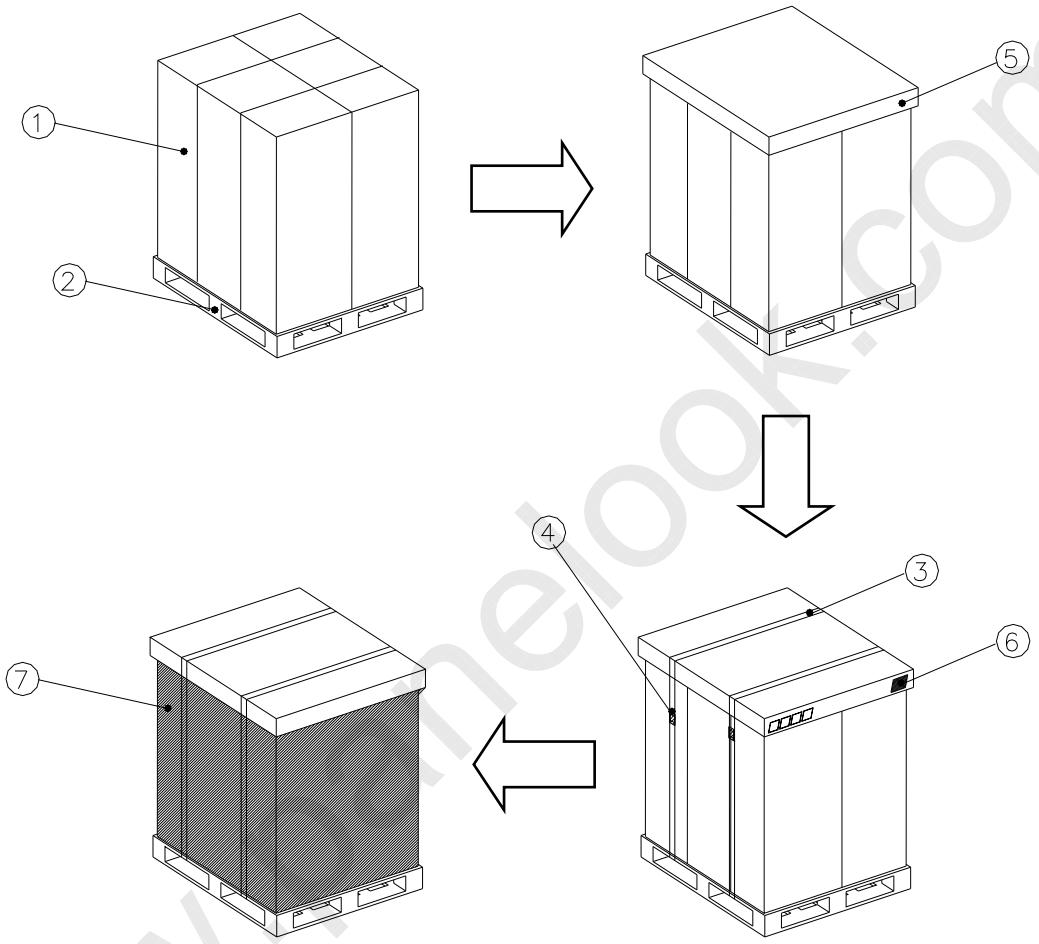
■ Packing Ass'y



NO.	DESCRIPTION	MATERIAL
1	LCM	
2	BAG	AL
3	TAPE	OPP
4	PACKING, BOTTOM	EPS
5	PACKING, TOP	EPS
6	BOX	PAPER, SW
7	TAPE	OPP
8	LABEL	ART

APPENDIX-I-2

■ Pallet Ass'y



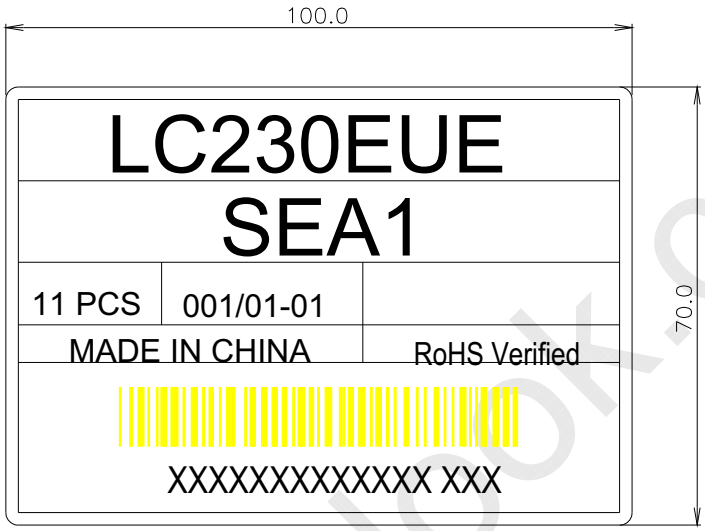
NO.	DESCRIPTION	MATERIAL
1	PACKING ASS'Y	
2	PALLET	PLYWOOD_1140X870X117.5
3	BAND	PP
4	CLIP, BAND	STEEL
5	ANGLE, PACKING	PAPER (SW)
6	LABEL	PAPER
7	Wrapping	LLDPE

 # APPENDIX- II-1

■ LCM Label

 # APPENDIX- II-2

■ Box Label



■ Pallet Label